PATENT ABSTRACTS

[File 347] JAPIO Dec 1976-2007/Dec(Updated 080328)

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[File 350] Derwent WPIX 1963-2008/UD=200830

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; d s
Set Items Postings Description
    903534 4342665 S TEST???
S1
    3303
          26092 S MERG???(2N)(DATA OR OUTPUT? OR OUT()PUT? ? OR BIT OR BITS OR
S2
PATTERN? ? OR SIGNAL? ?)
     2672 20921 S (DOUBLE OR DUAL)(2N)DATA()RATE OR DDR
S3
S4
     654
           3933 S SINGLE()DATA()RATE OR SDR
S5
     405
           3316 S S2(3N)(GENERAT??? OR MAKE? ? OR MAKING OR MADE OR CREAT???)
S6
    68061
           628653 S (CLOCK OR EDGE? ?)(3N)(RISE? ? OR RISING OR LEAD??? OR FALL???
OR TRAIL???)
           804 S S1 AND S5
S7
      32
S8
      1
           92 S S7 AND (S3:S4 OR S6)
S9
      31
           764 S S7 NOT S8
            3853 S MERG???(3N)(OUTPUT? OR OUT()PUT????)
S10
      769
S11 4101018 28274510 S CIRCUIT?? OR IC OR SEMICONDUCT??? OR CHIP? ?
S12
      218
           4977 S S10 AND S11
            394 S S10(3N)(GENERAT??? OR MAKE? ? OR MAKING OR MADE OR CREAT???)
S13
      57
           627 S S13 AND S11
S14
      18
S15
      15
            569 S S14 NOT S7
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FULL-TEXT PATENTS

[File 348] EUROPEAN PATENTS 1978-2007/ 200818

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[File 349] PCT FULLTEXT 1979-2008/UB=20080508|UT=20080501

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; d s
Set Items Postings Description
    722915 9467629 S TEST???
S1
    5230 47612 S MERG???(2N)(DATA OR OUTPUT? OR OUT()PUT? ? OR BIT OR BITS OR
S2
PATTERN? ? OR SIGNAL? ?)
S3
     6065
           32436 S (DOUBLE OR DUAL)(2N)DATA()RATE OR DDR
S4
     1646
           14073 S SINGLE()DATA()RATE OR SDR
S5
           4296 S S2(3N)(GENERAT??? OR MAKE? ? OR MAKING OR MADE OR CREAT???)
     464
    68180 1218470 S (CLOCK OR EDGE? ?)(3N)(RISE? ? OR RISING OR LEAD??? OR
S6
FALL??? OR TRAIL???)
           144 S S1(50N)S5
S7
      19
S8
      26
           245 S S1(100N)S5
S9
            0 S S8(100N)(S3:S4 OR S6)
      0
$10 1029260 28905493 $ CIRCUIT?? OR IC OR SEMICONDUCTOR? ? OR CHIP? ? OR
PROCESSOR? ? OR MICROPROCESSOR? ?
           159 S S8(100N)S10
S11
S12
     1118
            6605 S MERG???(3N)(OUTPUT? OR OUT()PUT????)
S13
      71
            483 S S12(3N)(GENERAT??? OR MAKE? ? OR MAKING OR MADE OR CREAT???)
            107 S S13(100N)S10
S14
      13
S15
      13
            107 S S14 NOT S11
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[File 2] INSPEC 1898-2008/Apr W2 (c) 2008 Institution of Electrical Engineers. All rights reserved. [File 6] NTIS 1964-2008/May W3 (c) 2008 NTIS, Intl Cpyrght All Rights Res. All rights reserved. [File 8] Ei Compendex(R) 1884-2008/Apr W4 (c) 2008 Elsevier Eng. Info. Inc. All rights reserved. [File 23] CSA Technology Research Database 1963-2008/Apr (c) 2008 CSA. All rights reserved. [File 34] SciSearch(R) Cited Ref Sci 1990-2008/May W2 (c) 2008 The Thomson Corp. All rights reserved. [File 35] Dissertation Abs Online 1861-2008/Nov (c) 2008 ProQuest Info&Learning. All rights reserved. [File 65] Inside Conferences 1993-2008/May 12 (c) 2008 BLDSC all rts. reserv. All rights reserved. [File 95] TEME-Technology & Management 1989-2008/Apr W3 (c) 2008 FIZ TECHNIK. All rights reserved. [File 99] Wilson Appl. Sci & Tech Abs 1983-2008/Apr (c) 2008 The HW Wilson Co. All rights reserved. [File 144] Pascal 1973-2008/May W1 (c) 2008 INIST/CNRS. All rights reserved. [File 256] TecInfoSource 82-2008/Feb (c) 2008 Info.Sources Inc. All rights reserved. [File 434] SciSearch(R) Cited Ref Sci 1974-1989/Dec (c) 2006 The Thomson Corp. All rights reserved. ; d s Set Items Postings Description 7852850 16126005 S TEST??? S2 6529 15733 S MERG???(2N)(DATA OR OUTPUT? OR OUT()PUT? ? OR BIT OR BITS OR PATTERN? ? OR SIGNAL? ?) S3 8791 14928 S (DOUBLE OR DUAL)(2N)DATA()RATE OR DDR **S4** 5331 12565 S SINGLE()DATA()RATE OR SDR S5 488 S \$2(3N)(GENERAT??? OR MAKE? ? OR MAKING OR MADE OR CREAT???) 156 **S**6 86628 327957 S (CLOCK OR EDGE? ?)(3N)(RISE? ? OR RISING OR LEAD??? OR FALL??? OR TRAIL???) 91 S S1 AND S5 **S**7 16 **S8** 153 S S2 AND (S3:S4 OR S6) 27 S9 244 S S7:S8 43 27 150 RD (unique items) S10 116 S S10 NOT PY=2004:2008 S11 21

\$12 4749021 12869523 \$ CIRCUIT?? OR IC OR CHIP? ? OR SEMICONDUCTOR? ? OR

PROCESSOR? ? OR MICROPROCESSOR? ?

S13 575 3276 S S2 AND S12 S14 0 S S13 AND INVERTOR?? 0 S15 116 1306 S S13 AND S1 69 S S13 AND (S4:S5 OR S6) S16 8 S17 6 49 RD (unique items) S18 395 882 S MERG???(3N)(OUTPUT? OR OUT()PUT????) S19 9 27 S S18(3N)(GENERAT??? OR MAKE? ? OR MAKING OR MADE OR CREAT???) 6 18 RD (unique items) S20 15 S S20 NOT (S11 OR S17) S21 5 S22 14 134 S S18 AND S12 AND (CLOCK??? OR CLOCKPULSE?) S23 8 85 RD (unique items) 7 79 S S23 NOT (S11 OR S17 OR S21) S24